

Electronic Acknowledgement Receipt

EFS ID:	7752525
Application Number:	10575758
International Application Number:	
Confirmation Number:	5126
Title of Invention:	Printed Circuit Board Design Instruction Support Method and Device
First Named Inventor/Applicant Name:	Hiromichi Inaishi
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Filer Authorized By:	Paul C. Lewis
Attorney Docket Number:	1794-0182PUS1
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		2010604RCE.PDF	268445 8f1f14d4bdc0f74f7634f4e1f62c1dd4c812 b986	yes	5
Multipart Description/PDF files in .zip description					
	Document Description		Start		End
	Request for Continued Examination (RCE)		1		1
	Extension of Time		2		2
	Fee Worksheet (PTO-875)		3		3
	Miscellaneous Incoming Letter		4		5

Warnings:**Information:**

2	Fee Worksheet (PTO-875)	fee-info.pdf	32091 868f71678a169c831a86509c75551b16105 1844	no	2
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Warnings:**Information:**

Total Files Size (in bytes):	300536
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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